

# ELECTRONIC INFORMATION DISCLOSURE STATEMENT

Electronic Version v18

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## Title of Invention

## INPUT STRUCTURE FOR A POWER AMPLIFIER AND ASSOCIATED METHODS

Application Number : 10/743220



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First Named Applicant: Alan Westwick

Attorney Docket Number: SIL.P0056

Art Unit: 2817

Examiner: PATRICIA T NGUYEN

Search string: ( 6809581 or 6259325 or 4024346 or 4705967 or 6756849 or 6549071 or 6448847 or 6727754 or 6828859 or 6894565 or 6897730 or 20050068103 ).pn

## US Patent Documents

**Note: Applicant is not required to submit a paper copy of cited US Patent Documents**

init	Cite.No.	Patent No.	Date	Patentee	Kind	Class	Subclass
	1	6809581	2004-10-01	Rofougaran et al.			
	2	6259325	2001-07-01	Ishizuka et al.			
	3	4024346	1977-05-01	McDermott, Noel C.			
	4	4705967	1987-11-01	Vasile, Carmine F.			
	5	6756849	2004-06-29	Dupuis et al.			
	6	6549071	2003-04-15	Paul et al.			
	7	6448847	2002-09-10	Paul et al.			
	8	6727754	2004-04-27	Dupuis et al.			
	9	6828859	2004-12-07	Dupuis et al.			
	10	6894565	2005-05-17	Bocock et al.			
	11	6897730	2005-05-24	Dupuis et al.			

## US Published Applications

**Note: Applicant is not required to submit a paper copy of cited US Published Applications**

init	Cite.No.	Pub. No.	Date	Applicant	Kind	Class	Subclass
	1	20050068103	2005-03-31	Dupuis et al.			

**Signature**

Examiner Name	Date